Consider a wafer with

* Defect density *d = 1.25 defects/cm2*
* clustering parameter α *= 0.5* and Chip area, *A = 8 mm × 8 mm = 0.64 cm2*
* each wafer has 800 chips
* The cost of processing a wafer is $250
  1. Calculate the processing cost if Design for testability (DFT) is included, which increases the chip area by 20%.